

MITHA M

Research Scholar

Department of Electronics
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Career Objective: To achieve Excellence in Teaching and Research and strive for the holistic development of students and thereby render service to the Nation.

EDUCATIONAL QUALIFICATIONS

Qualification	Institute/University	Year
M Tech (Digital Electronics and Communication Systems)	Malnad College of Engineering, Hassan (Visvesvaraya technological university, Belgaum)	2017
B E (Electronics and Communication)	Sri Jayachamarajendra College of Engineering, Mysuru (Visvesvaraya technological university, Belgaum)	2006

Additional Qualification: GATE 2015, NET July 2018

Paper Publications

1. *A Review on BSNL's Next Generation Networks* in the **IEEE**-International Conference on Innovative Mechanisms for Industry Applications (ICIMIA 2017) conducted by Dayananda Sagar College of Engineering from 21st to 23rd February 2017 published in **IEEE Xplore** on July 2017.
2. “*Real Time Implementation of Multimedia Traffic Using MUX in Optical Networks*”, International Conference on Fiber Optics and Photonics – 2018 held during 12th and 15th December 2018 at IIT Delhi, Delhi, India (ISBN 978-93-88653-41-1)

EXPERIENCE

Years of Experience in teaching: 6 Years

S. N.	Institute/University	Period of service in each post	Designation	Name of work and level of responsibility
1	Dos in Electronics, Hemangothri, Hassan (University of Mysore)	Sept 2018 to March 2021	Guest faculty	PG MSc in Electronics
2	Adichunchanagiri Polytechnic, Chikkamagaluru (Dept. of Technical Education)	July 2014 to May 2015	Lecturer	Diploma in Electronics and Communications
3	M H Institute of management Sciences, Ramanagara (Bangalore University)	March 2011 to April 2014	Asst. Lecturer	UG BCA
4	Field Marshall Cariappa College, Madikeri (Mangalore University)	December 2007 to November 2008	Guest faculty	UG BCA and BSc

SUBJECTS TAUGHT

Basics of Electrical and Electronics Engineering, Basic Electronics, Digital Electronics, Medical Electronics, Analog Communications, Digital Communications, Microprocessor Architecture and programming, Computer Graphics, Analog circuit design, Digital circuit design, Computer networks, Optical fiber communication, CMOS VLSI, Low power VLSI.

SEMINARS/WORKSHOPS ATTENDED

Sl.No	Course title	Institute	No of days	Duration
1	Antenna Modeling & Simulation using FEKO and IEEE Authorship	IEEE APS Student Branch Chapter - RVCE IEEE AP-MTT Chapter and IEEE Bangalore Section	6 days	22 nd -27 th February, 2021
2	National level workshop, Data Analytics and Learning(NWDAL-2019) on Artificial Intelligence(AI),Machine Learning(ML) and Deep Learning(DL)	Department of Studies in Computer Science, University of Mysore	3 days	18 th to 20 th Nov 2019
3	Antenna design using HFSS EM simulator	MCE, Hassan	1 day	25 th March 2017
4	Image processing and pattern recognition techniques	MCE, Hassan	3 days	24 th to 26 th March 2017
5	Research methodology and intellectual property rights	MCE, Hassan	3 days	23 rd to 25 th March 2017
6	Moems and applications	MCE, Hassan	1 day	18 th March 2017
7	Internet of things for real time applications	MCE, Hassan	2 days	13 th and 14 th March 2017
8	Virtual laboratory	MCE, Hassan	1 day	16 th Feb 2017

PERSONAL PROFILE

Name : Mitha M

Date of Birth : 31st January 1983

Nationality : Indian

Postal Address : Arundhathi Nilaya, Mother Teresa Road, Vidya
Nagar, Hassan, Karnataka, India -573201

Marital Status : Married

(MITHA M)